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EITAN LAW GROUP
c/o LANDON IP INC.
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11/15/2005 MBEYENE2 00000075 503400 10695457

01 FC:1501 1400.00 DA
02 FC:1504 300.00 DA
03 FC:8001 21.00 DA

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/695,457	October 29, 2003	COHEN, Guy	P-5487-US1	4578

TITLE OF INVENTION: A METHOD CIRCUIT AND SYSTEM FOR READ ERROR DETECTION IN A NON-VOLATILE MEMORY ARRAY

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DUE DATE
nonprovisional	NO	\$1400	\$300	\$1700	12/08/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
MAI. SOON LUU	2827	365-185200

1 Change of correspondence address or indication of "Fee Address" (37 CFR 1.363)
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2 _____

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3 ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(I) NAME OF ASSIGNEE

Saifun Semiconductors Ltd

(II) RESIDENCE: (CITY and STATE OR COUNTRY)

Netanya, Israel

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☐ Corporation or other private group entity ☐ Government

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- ☒ Issue Fee
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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27 ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Authorized Signature: Date: November 10, 2005

Typed or printed name: Vladimir Sherman

Registration Number: 43,116

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